

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/035,574
				Filing Date	October 18, 2001
				First Named Inventor	Mark Anders
				Art Unit	2611
				Examiner Name	Sudhanshu C. Pathak
Sheet	2	of	2	Attorney Docket Number	42390P11426

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
SP		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", Symposium on VLSI Circuits, Digest of Technical Papers, pp. 16-17 (Jun. 13-15, 2002).	
SP		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", IEEE Journal of Solid-State Circuits, Vol. 38, No. 5, pp. 709-714 (May 2003).	
SP		Stan, Mircea R., et al., "Bus-Invert Coding for Low-Power I/O", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 3, No. 1, pp. 49-58 (Mar. 1995).	

Examiner Signature	/Sudhanshu Pathak/	Date Considered	08/04/2006
--------------------	--------------------	-----------------	------------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

